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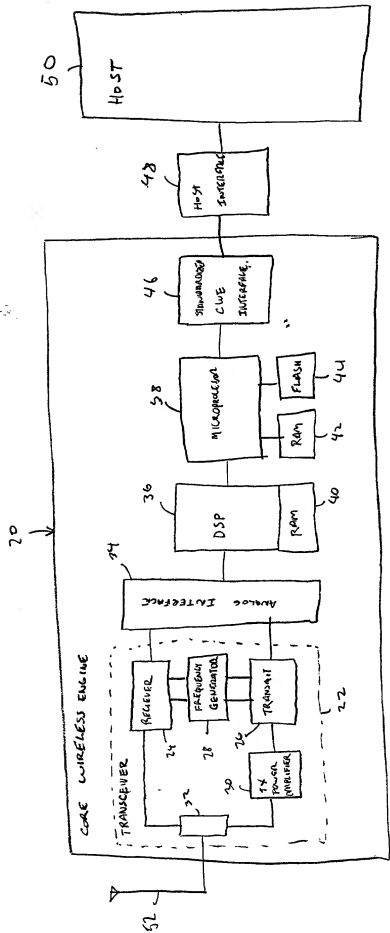


FIGURE 1

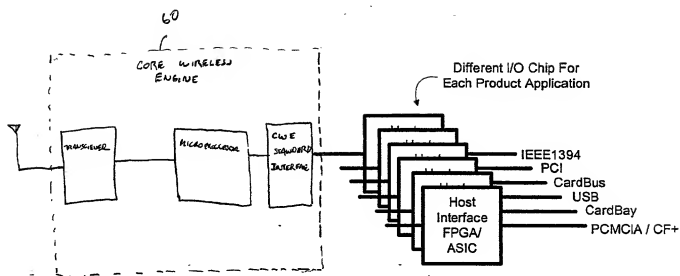


FIGURE 2

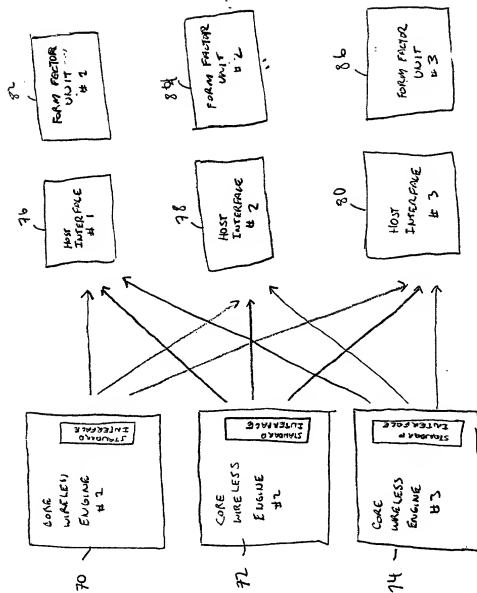


FIGURE 3

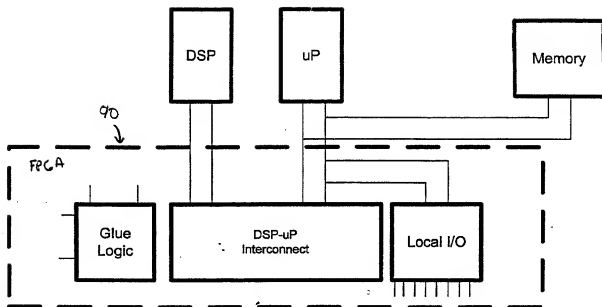


FIGURE 4A

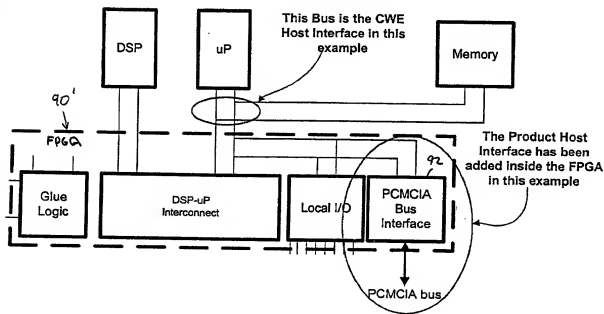


FIGURE 4B

Registers Visible to CWE MCU across CWE Host Interface

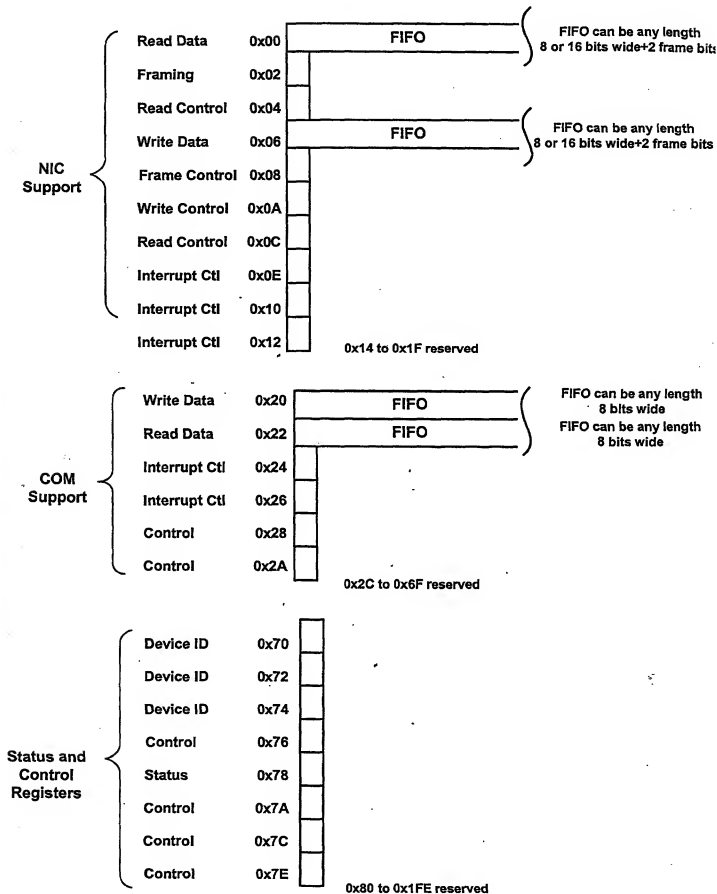


FIGURE 5

CWE Host Interface

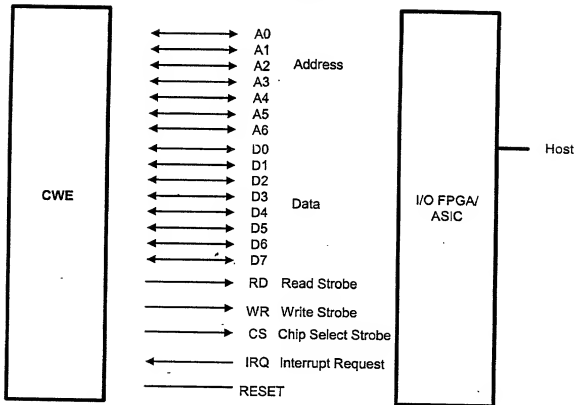
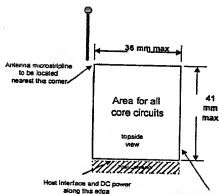
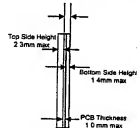


FIGURE 6



Board Thickness = bottomside height

± 2.3 mm max



Notes:

- 1) RF connector and host connector fall outside the defined area, but nearest the locations shown
- 2) Shielding is to be included within the dimensions shown
- 3) Indicator LEDs, test connectors and other optional components may be outside the dimensions shown
- 4) PCB thickness at host connector will require additional buildup to 1 mm when building a Mini-PCI type 3 product. If core design has PCI test, then 1.0 mm thick

FIGURE 7

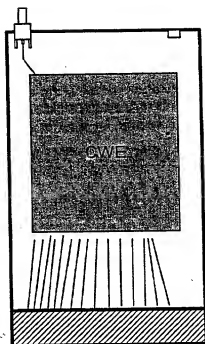


FIGURE 8A

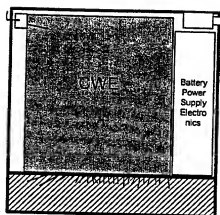


FIGURE 8B

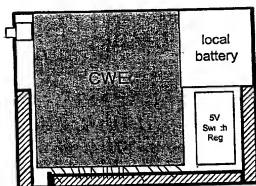


FIGURE 8C

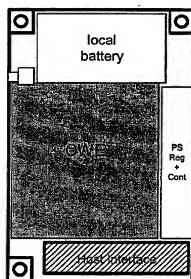


FIGURE 8D

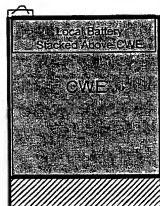


FIGURE 8E

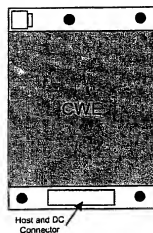


FIGURE 8F